

**What is claimed is:**

1. A method comprising:  
producing a combination by selecting a pre-determined  
number of bits from two or more bit streams;  
5 determining a symbol corresponding to the combination  
from a symbol set comprising at least sixteen symbols; and  
transmitting a signal corresponding to the symbol at  
a rate of at least 10 giga bits per second over a  
communication medium.
- 10 2. The method of claim 1 a first bit stream  
comprises  $N$  bits and a second bit stream comprises  $N/3$   
bits.
3. The method of claim 2 the  $N$  bits are generated by  
encoding  $K$  bits using low density parity check coding  
15 technique.
4. The method of claim 1 the combination comprises  
four bits wherein one bit is selected from the  $N/3$  bits  
and three bits is selected from the  $N$  bits, wherein the  
number of bits of the combination is based on an allowable  
20 bit error rate at a measured value of signal-to-noise  
ratio of the communication medium.
5. The method of claim 1 determining the symbol  
comprises selecting the symbol based on a bit-to-symbol  
mapping table.
- 25 6. The method of claim 5 the selecting comprises  
searching for a matching entry corresponding to the  
combination and producing the symbol corresponding to the  
matching entry.
7. The method of claim 1 further comprising:  
30 generating a coded bit stream and an un-coded bit  
stream from a received signal;  
decoding a coded bit stream to generate an extracted  
bit stream based on the reliability assignment values

determined empirically,

generating a bit stream representing data, and  
sending the bit stream.

8. The method of claim 7 the reliability assignment  
5 values are chosen based on the bit position of the coded  
bit stream corresponding to the symbol being decoded and  
one or more pre-defined reliability assignment values.

9. An apparatus comprising:

a transmitter to produce a combination by selecting a  
10 pre-determined number of bits from one or more bit  
streams, to determine a symbol corresponding to the  
combination from a symbol set, wherein the symbol set  
comprises at least sixteen symbols;

a receiver to demodulate based on a reliability  
15 assignment, and

a front end device to transmit a signal corresponding  
to the symbol at a rate of at least 10 giga bits per  
second over a communication medium.

10. The apparatus of claim 9 wherein the transmitter  
20 comprises a mapper to receive a first bit stream  
comprising  $N$  bits and a second bit stream comprising  $N/3$   
bits.

11. The apparatus of claim 10 further comprises an  
encoder to generate  $N$  bits by encoding  $K$  bits using low  
25 density parity check coding technique.

12. The apparatus of claim 9 the transmitter  
comprises the mapper to generate the combination by  
selecting one bit out of the  $N/3$  bits and three bits out  
of the  $N$  bits, wherein the number of bits of the  
30 combination is based on an allowable bit error rate at a  
measured value of signal-to-noise ratio of the  
communication medium.

13. The apparatus of claim 9 the transmitter

comprises the mapper to determine the symbol by selecting the symbol based on the bit-to-symbol mapping table.

14. The apparatus of claim 13 the mapper to select the symbol comprises a content addressable memory to  
5 search a matching entry corresponding to the combination and to produce the symbol corresponding to the matching entry.

15. The apparatus of claim 9 the receiver further comprising:

10 a de-mapper to generate a coded bit stream and an uncoded bit stream from a received signal;

a decoder to decode a coded bit stream to generate an extracted bit stream based on the reliability assignment values determined empirically, and

15 a de-framer to generate a bit stream representing data.

16. The apparatus of claim 15 the reliability assignment values are chosen based on the bit position of the coded bit stream corresponding to the symbol being  
20 decoded and one or more pre-defined reliability assignment values.

17. The apparatus of claim 9 corresponds to a transceiver.

18. A system comprising:

25 a network interface to generate and transfer a signal that is representative of a bit stream and that comprises symbols selected from at least one of sixteen symbols,

a processor to provide the network interface with the  
30 bit stream in response to executing instructions;

a memory to store the instructions executed by the processor.

19. The system of claim 18 wherein the

network interface is to generate the signal as an amplitude modulated signal having a rate of at least 10 giga bits per second.

20. The system of claim 19 the network interface  
5 further comprising a transmitter to produce a combination by selecting a pre-determined number of bits from one or more bit streams, to determine the symbol corresponding to the combination from a symbol set, wherein the symbol set comprises at least sixteen symbols; and

10 a front end device to transmit the signal corresponding to the symbol at a rate of at least 10 giga bits over the communication medium.

21. The system of claim 20 wherein the transmitter comprises a mapper to receive a first bit stream  
15 comprising  $N$  bits and a second bit stream comprising  $N/3$  bits.

22. The system of claim 20 the transmitter comprises the mapper to generate the combination by selecting one bit out of the  $N/3$  bits and three bits out of the  $N$  bits.

20 23. The system of claim 20 the transmitter comprises the mapper to determine the symbol by selecting the symbol based on the bit-to-symbol mapping table.

24. The system of claim 20 the mapper to select the symbol comprises a content addressable memory to search a  
25 matching entry corresponding to the combination and to produce the symbol corresponding to the matching entry.

25. The system of claim 19 the network interface further comprises a receiver comprising:

a de-mapper to generate a coded bit stream and an un-  
30 coded bit stream from a received signal;

a decoder to decode a coded bit stream to generate an extracted bit stream based on the reliability assignment values determined empirically, and

a de-framer to generate a bit stream representing data.

26. The system of claim 19 the network interface includes a network interface card.

5 27. The system of claim 26 the network interface card includes logic capable of communicating at least in accordance with 10GBase-T standard.

28. The system of claim 18 the system further includes at least one of a computer, a switch, a router,  
10 or a server.